

REMARKS

The drawings are objected to under 37 CFR 1.83(a). this objection is overcome in view of the amendments to claim 10, 24 and 27.

Claims 1-28 are rejected under 35 U.S.C. 112 with regard to the recitation "SMBus" in claims 1, 15, 25 and 28, and with regard to the reference to "Standard" in claims 6, 7, 20 and 21. This rejection is no longer applicable in view of the amendments to claims 1, 15, 25 and 28 and the cancellation of claims 6, 7, 20 and 21.

Claims 1-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Harari et al (U.S. 6,981,068) and Larson et al (U.S. 6,968,470). This rejection is no longer applicable to the amended claims.

Claims 1, 11, 25 and 28 include: a first port electrically coupled to the first subsystem by a first connector, wherein the first connector includes at least one pin terminal to transfer the power and at least one pin terminal coupled to a System Management Bus (SMBus) of the computer; a second port electrically coupled to the second subsystem by a second connector, wherein the second connector includes at least one pin terminal to transfer the power; a transfer component electrically coupled to the first and second ports, wherein the transfer component is operable to transfer the power; a memory component electrically coupled to the SMBus, the memory component storing an identifier to uniquely identify the device, wherein an amount of the power received by the device is adjusted in response to the identifier; a detection component operable to detect the device; and a control component operable to dynamically adjust the amount of the power transferred to the at least one pin terminal of the first connector in response to the detection of the device.

As the PTO recognizes in MPEP §2142:

The Examiner bears the initial burden of factually supporting any *prima facie* conclusion of obviousness. If the examiner does not produce a *prima facie* case, the applicant is under no obligation to submit evidence of nonobviousness.

The USPTO clearly cannot establish a *prima facie* case of obviousness in connection with the amended claims for the following reasons.

35 U.S.C. §103(a) provides that:

[a] patent may not be obtained ... if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains ... (emphasis added)

Thus, when evaluating a claim for determining obviousness, all limitations of the claim must be evaluated. However, the references, alone, or in combination, do not teach a control component operable to dynamically adjust the amount of power transferred to the at least one pin terminal of the first connector in response to detection of the device.

Therefore, it is impossible to render the subject matter of the claims as a whole obvious based on a single reference or any combination of the references, and the above explicit terms of the statute cannot be met. As a result, the USPTO's burden of factually supporting a *prima facie* case of obviousness clearly cannot be met with respect to the claims, and a rejection under 35 U.S.C. §103(a) is not applicable.

There is still another compelling, and mutually exclusive, reason why the references cannot be combined and applied to reject the claims under 35 U.S.C. §103(a).

The PTO also provides in MPEP §2142:

[T]he Examiner must step backward in time and into the shoes worn by the hypothetical "person of ordinary skill in the art" when the invention was unknown and just before it was made. In view of all factual information, the Examiner must then make a determination whether the claimed invention "as a whole" would have been obvious at that time to that person. ...[I]mpermissible hindsight must be avoided and the legal conclusion must be reached on the basis of the facts gleaned from the prior art.

Here, the references do not teach, or even suggest, the desirability of the combination because neither teaches or suggests providing a control component operable to dynamically adjust the amount of power transferred to the at least one pin terminal of the first connector in response to detection of the device.

Thus, neither of these references provides any incentive or motivation supporting the desirability of the combination. Therefore, there is simply no basis in the art for combining the references to support a 35 U.S.C. §103(a) rejection of the claims.

In this context, the MPEP further provides at §2143.01:

The mere fact that references can be combined or modified does not render the resultant combination obvious unless the prior art also suggests the desirability of the combination. (emphasis in original)

In the above context, the courts have repeatedly held that obviousness cannot be established by combining the teachings of the prior art to produce the claimed invention, absent some teaching, suggestion or incentive supporting the combination. In the present case it is clear that the USPTO's combination arises solely from hindsight based on the invention without any showing, suggestion, incentive or motivation in either reference for the combination as applied to the claims. Therefore, for this mutually exclusive reason, the USPTO's burden of factually supporting a *prima facie* case of obviousness clearly cannot be met with respect to the claims, and the rejection under 35 U.S.C. §103(a) is not applicable.

In view of all of the above, the allowance of claims 1, 3-5, 8-19 and 22-28 is respectfully requested.

The Examiner is invited to call the undersigned at the below-listed telephone number if a telephone conference would expedite or aid the prosecution and examination of this application.

Respectfully submitted,



James R. Bell

Registration No. 26,528

Dated: 6-14-06
HAYNES AND BOONE, LLP
901 Main Street, Suite 3100
Dallas, Texas 75202-3789
Telephone: 512/867-8407
Facsimile: 214/200-0853
ipdocketing@haynesboone.com